FIG. 1

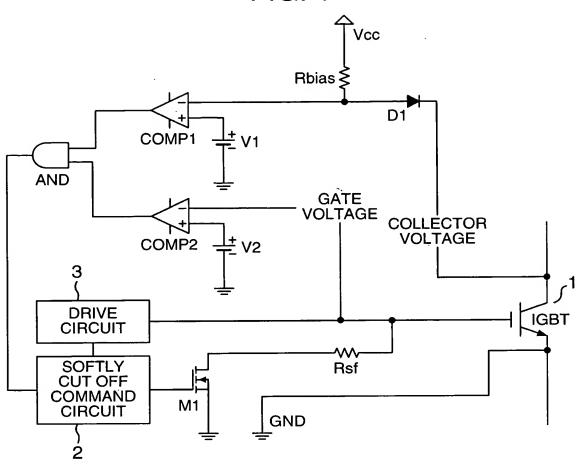


FIG. 2 SHORT CIRCUIT WAVEFORMS SHORT CIRCUIT WHEN PRESENT INVENTION IS DETECTION **APPLIED** SECOND REFERENCE VOLTAGE V2=TERRACED TERRACED **GATE** VOLTAGE + α VOLTAGE **VOLTAGE** COLLECTOR FIRST REFERENCE (B) VOLTAGE **VOLTAGE V1** RATED CURRENT×6 **COLLECTOR** (C) CURRENT T1 T2 SHORT CIRCUIT

FIG. 3

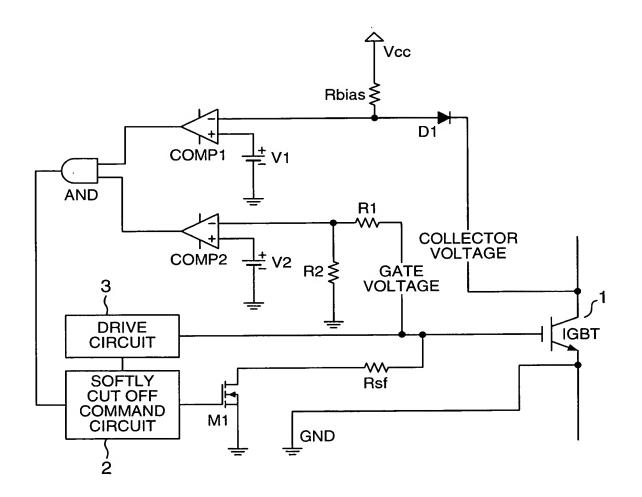


FIG. 4

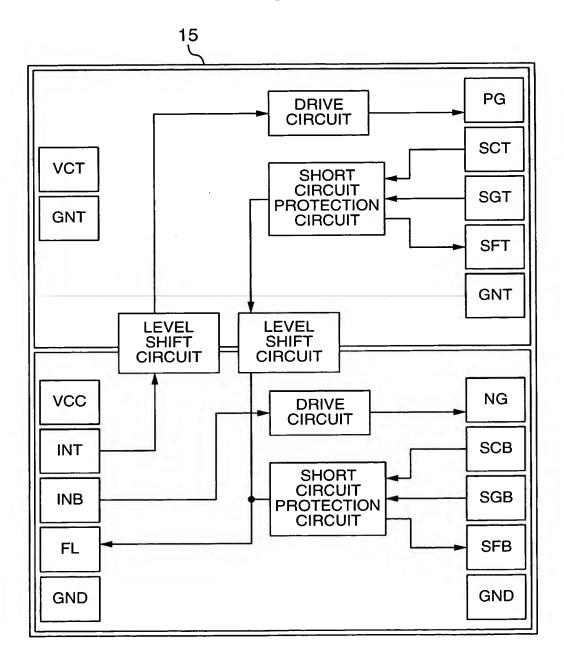


FIG. 5

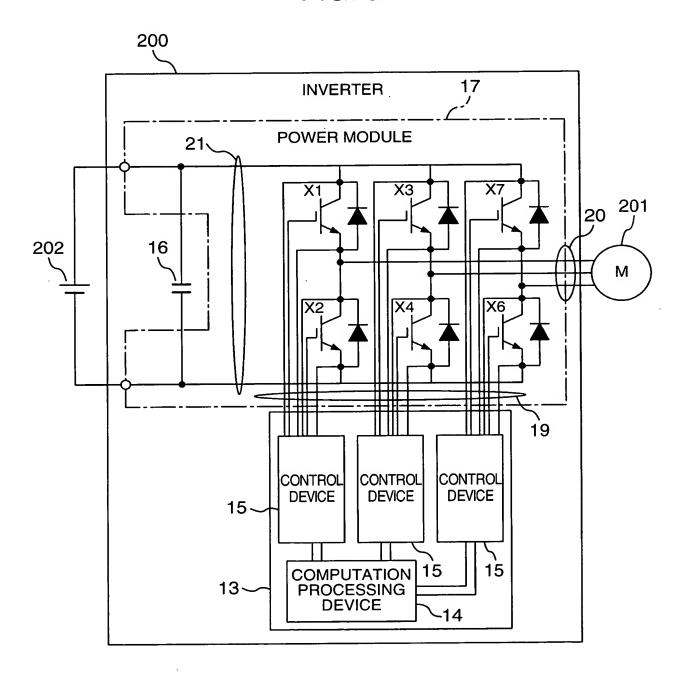


FIG. 6

